

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

KWANG-JIN YANG et al.

Serial No.:

09/621,009

Examiner:

CURS, NATHAN M.

Filed:

20 July 2000

Art Unit:

2633

For:

BIT-RATE INDEPENDENT OPTICAL RECEIVER AND METHOD THEREOF

## **INFORMATION DISCLOSURE STATEMENT**

Paper No. 16

Commissioner for Patents P.O.Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes and provide copies of the following art references. Under 37 C.F.R. §1.98(a)(2) however, copies of U.S. patent reference(s) are not provided.

• Office action issued on 17 June 2005 by the German Patent Office in German patent application No. 102 31 943.2-31 which corresponds to the present application, U.S. Serial No. 09/621,009. An English language translation is attached.

The following two references were cited in the aforesaid German Office action:

 U.K. Patent Application GB 2 176 376 A to RCA Corporation (inventor: Jeffrey Paul Viola), entitled CLOCK RECOVERY CIRCUIT FOR DATA SYSTEMS, published on 17 December 1986. U.S. Patent No. 4,361,897 to Kloeber, entitled CIRCUIT ARRANGEMENT FOR
 CLOCK PULSE RECOVERY AT THE RECEIVING END OF DIGITAL
 CLOCK-CONTROLLED DATA TRANSMISSION SYSTEMS, issued on November
 30, 1982.

Viola '376 discloses that in a receiver suitable for a high data optical communication system, a stream of data bit signals from a data slicer is applied to a clock recovery circuit including an impulse generator for generating impulse signals from the bit stream. The impulse generator includes, for instance, a high speed ECL NOR gate driven by mutually out-of-phase and relatively delayed data. The impulse signals are applied, such as by way of a resistor and an input magnetic coupling loop, to ring a coaxial cavity.

Kloeber discloses that a circuit arrangement for clock pulse recovery on the receiving side of a digital clock-controlled data system is particularly characterized in that, in the receiver, a branch circuit is connected to the input side in parallel with a digital converter for converting the digital data signal, the branch circuit comprising an EXCLUSIVE or gate having two inputs and a delay element in the feed to one input of a digital counter which is connected to the output of the EXCLUSIVE OR gate and actuated by a quartz-stabilized oscillator having a frequency equal to a multiple of the desired timing frequency, the output of the oscillator being connected to the digital converter.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

PATENT P56077

It is submitted that each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

No fee is incurred by this Statement.

Respectfully submitted,

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Folio: P56077 Date: 8/17/05 I.D.: REB/kf

INFORMATION DISCLOSURE		SERIAL NUMBER	09/621009	DOCKET NO.	P56077	
PTO-1449 (PAGE 1 O		APPLICANT KWANG-JIN YANG et al.				
	AUG 1 7 2005	FILING DATE 20	July 2000	GROUP	2633	

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EXAMINER: DATE CONSIDERED:  EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of										
this form with next communication to applicant.										